

PATENT NUMBER

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>Am</u> Q.A. <u>W</u></p>	<p>PATENT DATE</p>
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APPLICATION NO: 09/684160	CONT/PRIOR	CLASS 711	SUBCLASS	ART UNIT 2185 2172	EXAMINER Hamilton
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APPLICANTS

James Merchant
Gordon Carskadon
Brian Evans
Jeffery Hunt

Method and system for generating a bit order data structure of configuration bits for a programmable logic device

PTO-2040
12/88

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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